

## KCO-532 Series

### 1.0mm Height Ceramic SMD Crystal Clock Oscillators

Description	KCO-532(5.0V)	KCO-532(3.3V)
Frequency range	0.5 ~ 75.0 MHz	0.5 ~ 125.0 MHz
Frequency stability	± 50ppm, ± 100ppm	
Operating temperature range	0 ~ +70	
Input voltage	5.0V ± 0.5V	3.3V ± 0.3V
Input current	10(*15) mA Max. (0.5~14.9 MHz) 15(*20) mA Max. (15~29.9 MHz) 25(*30) mA Max. (30~39.9 MHz) 35(*40) mA Max. (40~49.9 MHz) 40(*50) mA Max. (50~59.9 MHz) 40(*60) mA Max. (60~75.0 MHz)	10(*10) mA Max. (0.5~14.9 MHz) 15(*15) mA Max. (15~29.9 MHz) 20(*25) mA Max. (30~39.9 MHz) 25(*30) mA Max. (40~49.9 MHz) 30(*35) mA Max. (50~59.9 MHz) 40(*45) mA Max. (60~79.9 MHz) 50(*60) mA Max. (80~99.9 MHz) 60(*70) mA Max. (100.0~125.0 MHz)
Output Load	CMOS/TTL 15pF(*50pF)/2~10TTL	CMOS/TTL 15pF(*50pF)/2~10TTL
Rise & Fall Time	10ns Max(0.5~40.0 MHz) 7ns Max(50.0~100.0 MHz)	10ns (0.5~40.0 MHz) 7ns (50.0~125.0 MHz)
Duty Cycle	40:60	
Tristate	With or Without	

All specifications are subject to change without notice.

Option: \*Tighter Stability, \*Extended Temperature, \*Option 45:55 Duty Cycle

